



December 2010



- Pletronics' PE55F and PE55G Series is a quartz crystal controlled precision square wave generator with a fast rise and fall time PECL output.
- The package is designed for high density surface mount designs.
- Tape and Reel or cut tape packaging is available.
- 3.2 x 5 mm LCC Ceramic Package
- Enable/Disable Function on pad 1
- Disable function includes low standby power mode
- PE55F use Fundamental Mode Crystals 13MHz to 110MHz
- PE55G use 3rd Overtone Crystals 35MHz to 220MHz
- Low Jitter

Pletronics Inc. certifies this device is in accordance with the RoHS 6/6 (2002/95/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's

Weight of the Device: 0.16 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020D.1

Second Level Interconnect code: e4

Absolute Maximum Ratings:

Parameter	Unit
V _{cc} Supply Voltage	-0.5V to +5.0V
Vi Input Voltage	-0.5V to V _{CC} + 0.5V
Vo Output Voltage	-0.5V to V_{CC} + 0.5V
Junction Temperature (T _j)	-55°C to +150°C

Thermal Characteristics

The maximum die or junction temperature is 150°C

The thermal resistance junction to board is 30 to 50°C/Watt depending on the solder pads, ground plane and construction of the PCB.



December 2010

31

Part Number:

PE55	45	G	Ε	٧	-125.0M	-XX	
							Packaging code or blank T250 = 250 per Tape and Reel T500 = 500 per Tape and Reel T1K = 1000 per Tape and Reel
							Frequency in MHz
							Supply Voltage V _{cc} V = 3.3V <u>+</u> 10%
							Optional Enhanced OTR Blank = Temp. range -10 to +70°C C = Temp. range -20 to +70°C E = Temp. range -40 to +85°C
							Series Model F = Fundamental mode crystal G = 3 rd Overtone mode crystal
							Frequency Stability 45 = ± 50 ppm 44 = ± 25 ppm 20 = ± 20 ppm
							Series Model

Marking Legend:



P = Pletronics

ff.fff = Frequency in MHz

P = PECL

t = Mode of operation 'F' or 'G'

YMD = Date of Manufacture (year and week, or year-month-day)
All other marking is internal factory codes

Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from the marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD

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19

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Day

•	Jouc	odes for bate oode find																	
•	Code	0	1	2	3	4	Code	A	В	С	D	Е	F	G	Н	J	K	٦	M
	Year	2010	2011	2012	2013	2014	Mont	h JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC
	(Code		1	2	3	4	5	6	7	8	9	Α	В	С	D	Ε	F	G
		Day		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
	(Code		Н	J	K	L	М	N	Р	R	Т	U	٧	W	Х	Υ	Z	



December 2010

Electrical Specification for 3.30V ±10% over the specified temperature range

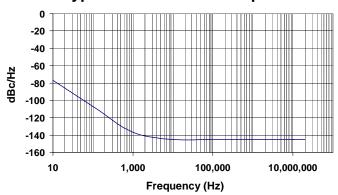
Item	Min	Тур	Max	Unit	Condition			
Frequency Range	13	-	110	MHz	For "F" series devices			
	35	-	220	MHz	For "G" series devices			
Frequency Accuracy "45"	-50	-	+50	ppm	For all supply voltages, load changes, agir			
"44"	-25	-	+25		for 1 year, shock, vibration and tempera			
" 20 "	-20	-	+20					
Supply Voltage Sensitivity	-2	-	2	ppm	For V _{CC} change of ±10%			
Output Waveform		L۱	/DS					
Output High Level (V _{OH})	-	1.43	1.60	volts	See load circuit			
Output Low Level (V _{OL})	0.90	1.10	-	volts	See load circuit			
Output Symmetry	45	1	55	%	output crossing point	See load circuit		
Output Swing	250	350	450	mV	See load circuit			
Jitter	1	1	0.6	pS RMS	12 KHz to 20 MHz from the	ne output frequency		
	ı	-	2.8	pS RMS	10 Hz to 1 MHz from the	output frequency		
Output T _{RISE} and T _{FALL}	1	150	400	pS	Vth is 20% and 80% of waveform			
V _{CC} Supply Current (I _{CC})	-	12 16	20 27	mA	< 80MHz ≥ 80MHz	"F" series devices		
		12 16 20 24	20 27 34 40	mA	< 90MHz ≥ 90 MHZ to > 125MHz ≥ 125MHz to > 160MHz ≥ 160MHz	"G" series devices		
Disable current	-	-10	-	uA	Pad 1 = 0.0 volts			
V disable	-	-	0.9	volts	Referenced to pad 3			
V enable	2.1	-	-	volts	Referenced to pad 3			
Output leakage V _{OUT} = V _{CC}	-10	-	+10	uA	Pad 1 low, device disable	d		
$V_{OUT} = 0V$	-10	-	+10	uA				
Enable time	1	-	2	mS				
Disable time	-	-	200	nS	Time for output to reach a	high Z state		
Start up time	-	-	2	mS	Time for output to reach specified frequency			
Operating Temperature	-10	-	+70	°C	Standard Temperature Range			
	- 20	-	+70	°C	Extended Temperature Range "C" Op			
	- 40	-	+85	°C	Extended Temperature Range "E" Option			
Storage Temperature	-55	-	+125	°C				
Standby Current I _{CC}	-	-	10	uA	Pad 1 low, device disable	d		

Specifications with Pad 1 E/D open circuit unless stated otherwise

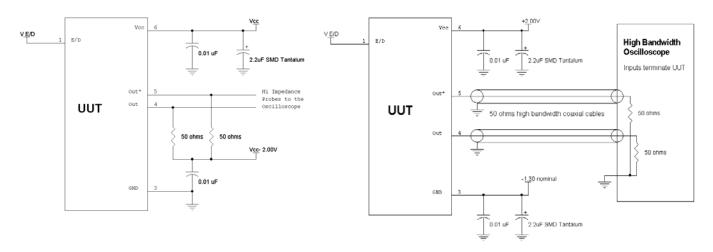


December 2010

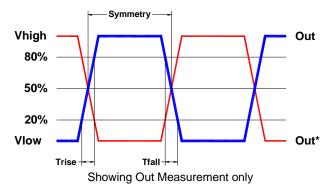
Typical Phase-Noise Response



Load Circuit



Test Waveform





December 2010

Reliability: Environmental Compliance

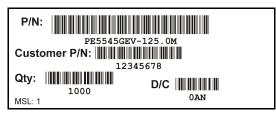
Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A

ESD Rating

Model	Minimum Voltage	Conditions
Human Body Model	1500	MIL-STD-883 Method 3115
Charged Device Model	1000	JESD 22-C101

Package Labeling

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII



Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant

2nd LvL Interconnect

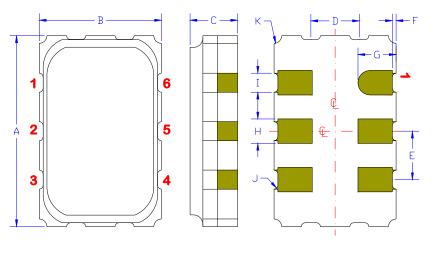
Category=e4

Max Safe Temp=260C for 10s 2X Max



December 2010

Mechanical:



Contacts: ¹ Ty Gold 11.8 to 39.4 μinches (0.3 to 1.0μm)

over Nickel 50 to 350 μinches (1.27 to 8.89 μm) ¹ Typical dimensions

Not to Scale

	Inches	mm
Α	0.197 <u>+</u> 0.006	5.00 <u>+</u> 0.15
В	0.125 <u>+</u> 0.006	3.20 <u>+</u> 0.15
С	0.053 max	1.35 max
D ¹	0.050	1.27
E ¹	0.050	1.27
F¹	0.004	0.10
G¹	0.039	1.00
H ¹	0.025	0.63
I ¹	0.020	0.50
J ¹	0.004R	0.10R
K¹	0.008R	0.20R

Pad	Function	Note
1	Output Enable/Disable	When this pad is not connected the oscillator shall operate. When this pad is <0.30 volts, the output will be inhibited (high impedance state.) Recommend connecting this pad to $V_{\rm CC}$ if the oscillator is to be always on.
2	No connect	There is no internal connection to this pad
3	Ground (GND)	
4	Output	Both outputs must be terminated and biased for proper operation. The ideal
5	Output*	termination is 50 ohms connected to 2.0V below the Supply Voltage.
6	Supply Voltage (V _{cc})	Recommend connecting appropriate power supply bypass capacitors as close as possible.

Lead free

Layout and application information

Recommend connecting Pad 1 and Pad 2 together to permit the design to accept Enable/Disable input on either pad

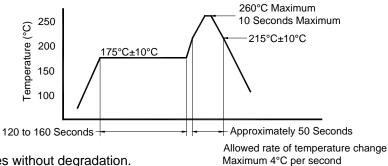
For Optimum Jitter Performance, Pletronics recommends:

- a ground plane under the device
- no large transient signals (both current and voltage) should be routed under the device
- do not layout near a large magnetic field such as a high frequency switching power supply
- do not place near piezoelectric buzzers or mechanical fans.



December 2010

Reflow Cycle (typical for lead free processing)



The part may be reflowed 3 times without degradation.

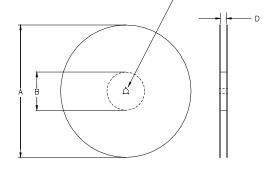
Tape and Reel: available for quantities of 250 to 1000 per reel, cut tape for < 250

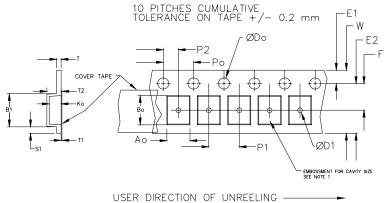
	Constant Dimensions Table 1											
Tape Size	D0	D1 Min	E1	P0	P2	S1 Min	T Max	T1 Max				
8mm		1.0			2.0							
12mm	1.5	1.5	1.75	4.0	<u>+</u> 0.05							
16mm	+0.1 -0.0	1.5	<u>+</u> 0.1	<u>+</u> 0.1	2.0	0.6	0.6	0.1				
24mm		1.5			<u>+</u> 0.1							

	Variable Dimensions Table 2											
Tape Size	B1 Max	E2 Min	F	P1	T2 Max	W Max	Ao, Bo & Ko					
16 mm	12.1	14.25	7.5 <u>+</u> 0.1	8.0 <u>+</u> 0.1	8.0	16.3	Note 1					

Note 1: Embossed cavity to conform to EIA-481-B

Dimensions in mm Not to scale





		REE			
Α	inches	7.0	10.0	13.0	
	mm	177.8	254.0	330.2	
В	inches	2.50	4.00	3.75	
	mm	63.5	101.6	95.3	Tape Width
С	mm	13	widin		
D	mm	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.0

Reel dimensions may vary from the above



December 2010

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